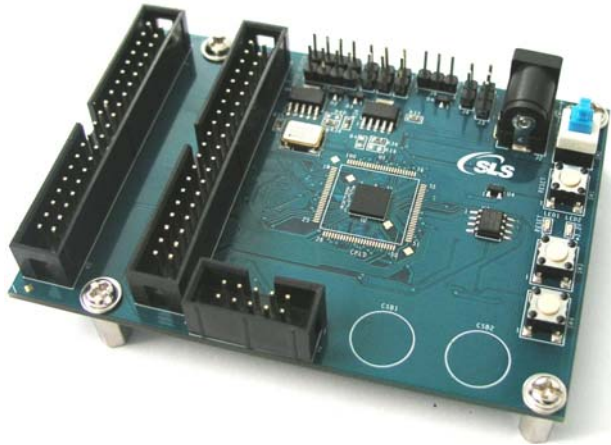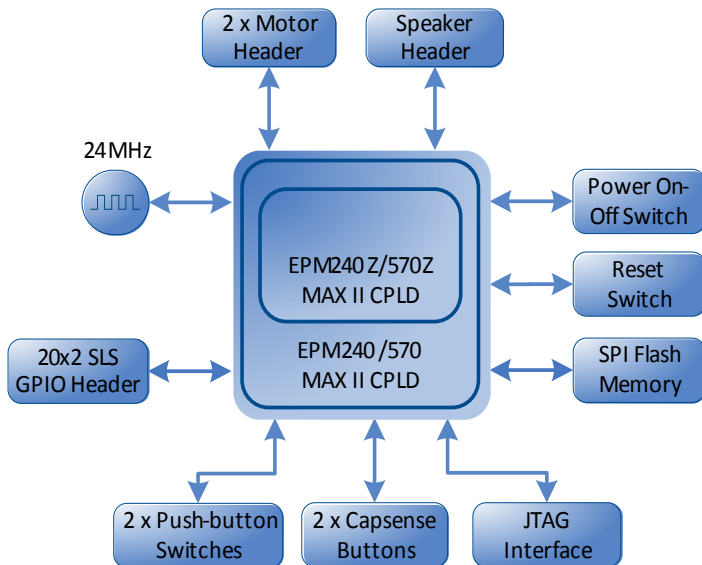# Development Board
MAX II/MAX IIZ

The MAX II/MAX IIZ Development board provides a hardware platform for designing and developing simple and low-end systems based on Altera MAX II/MAX IIZ Devices. The board features a MAX II/MAX IIZ EPM240T100Cx/EPM240ZM100Cx device with 240 Logic Elements (LEs) and 8,192 bits of User Flash Memory (UFM). The board also supports vertical migration with the EPM570T100Cx device with 570 Logic Elements (LEs) and 8,192 bits of User Flash Memory (UFM) for larger design support.

Board Features

- MAX II/MAX IIZ CPLD (Default : MAX IIZ)
- 64 General Purpose IOs
- Two capsense buttons and two push button switches
- User interface header
- External battery interface Header
- On board logic for MAX II/MAX IIZ configuration (Quartus II Compatible)
- On-board power supply
- On-board global system reset circuitry
- Migration support from EPM240T100Cx toEPM240ZM100Cx and EPM240T100Cx to EPM570T100Cx devices for higher density support for larger designs

Block Diagram

Deliverables

- MAX II/MAX IIZ development board (Default : MAX IIZ CPLD)
- USB BitJetLite Download Cable
- DC 5V/1.5A power supply
- Installation CD
  - Board reference design
  - Board reference manual

For more information on MAX II/MAX IIZ development board, visit http://www.slscorp.com/products/development-boards/max-iimax-iiz.html