

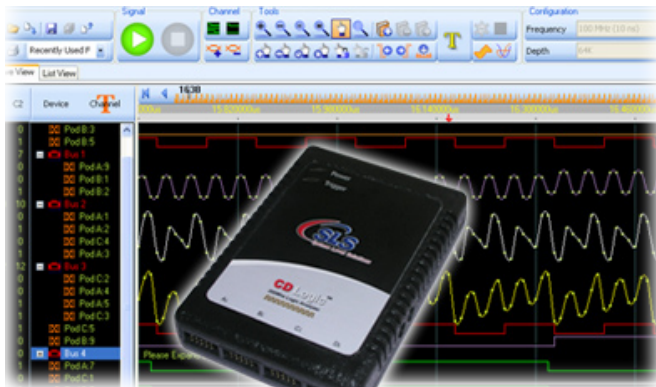
Digital Logic Analysis CDLogic

Powered by Altera FPGAs



A PC based logic analyzer, CDLogic (SLA2100), is a full featured high-performance tool with 36 channels and 100 MHz sampling frequency support for the troubleshooting and performance verification of digital circuits. The CDLogic is controlled with software that communicates via USB interface for capturing wide range of data simultaneously. It has multiple probes and it can capture and display digital signals at various voltage levels (3.3V, 5V or 7V) in bit, byte and word format.

The CDLogic software supports windows. This allows for more organized data display (with color coded data and increased screen size), intuitive user interface, and data management (file saving, loading, printing). The captured data is displayed in Waveform, List and Plug-in views.



Features

- Sampling rate: 500 Hz ~ 100 MHz
- Supports 36 Channels
- Samples: 64K ~ 512K per channel
- Protocol analysis facility for I C and USB
- Minimum sampling time (10ns)
- Output displays 25% pre-trigger and 75% post-trigger data
- External high speed clock input (max. 100 MHz)
- Displays samples (data) in List and Waveform View
- Facility to print waveforms
- Displays bus data in Digital as well as Analog form
- Comparison of bus data in Analog View
- Flexible trigger options (Channel and Bus)
- Voltage level can be defined for individual Bus
- All types of zooming supported
- Command prompt facility and full screen mode support
- Import/Export VCD (Value Change Dump) file and ASCII(Row Data) file
- Supports XML based file format
- LED indicators
- Supports USB interface
- OS support: Windows XP SP2, Windows 2K SP4
- Small physical size and easy to setup
- Low power consumption
- International language support

Applications

- Using CDLogic, one can debug and verify the following:
 - Microcontroller, Microprocessors
 - Digital Counters
 - State Machines
 - Buffers or memory circuits using SRAM, DRAM and FIFO etc.
 - Buses
 - System on Chip (SoC), functions in FPGA, standard cell or ASIC

Deliverables

- CDLogic Hardware
- 3 Micro grippers
- USB Cable
- 110V/240V Power adapter 6V/1A
- Four sets of cable assembly
- CD ROM
 - CDLogic Software
 - Documents

✓ Easy to use

Advanced Analysis

✓ Low Cost

New

✓ Analog bus view

Specifications	
Feature	Description
Channels	9/18/36
Buffer Size	64K/128K/256K/512K
Threshold Voltage	2.4V
External Clocks	100MHz max
Bandwidth	25 Msps
Maximum Input Voltage	5V
Sampling Rates	5sps upto 100Msps
OS Support	Windows XP SP2, Windows 2K SP4
Dimensions	4.5" x 5.2" x 1"

Download the software setup from <http://slscorp.com/pages/demodownload.php>

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