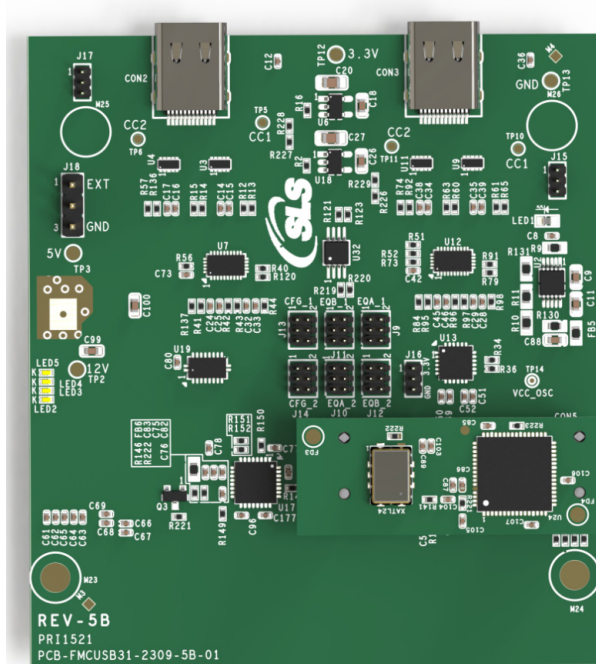


USB 3.2 FMC (FPGA Mezzanine Card) makes it easier to validate USB 3.2 functionality with FPGA having in-built serial transceivers. It also contains ULPI PHY chip to validate USB 2.0 functionality.



Features

- Standard Board Interface
 - FMC
- USB Interface
 - x2 USB Type C Connectors
 - ◆ CON2 supports USB 5Gbps & USB2.0 only
 - ◆ CON3 supports USB 10Gbps, USB 5Gbps and USB2.0
 - x2 USB Power switches to facilitate USB Host mode evaluation
- User Interface
 - x4 user defined LEDs
- Jumpers
 - For configuring TUSB1002 (USB 3.2 Linear re-driver and signal conditioner)
- Oscillator and Clock Generator Add on Board
 - 25 MHz clock oscillator with clock generator IC for USB 3.2 interface
- Easy to Use
 - Makes it easy to evaluate various SLS's USB IP cores

Deliverables

- USB 3.2 FMC
- USB 3.2 Type C Cable

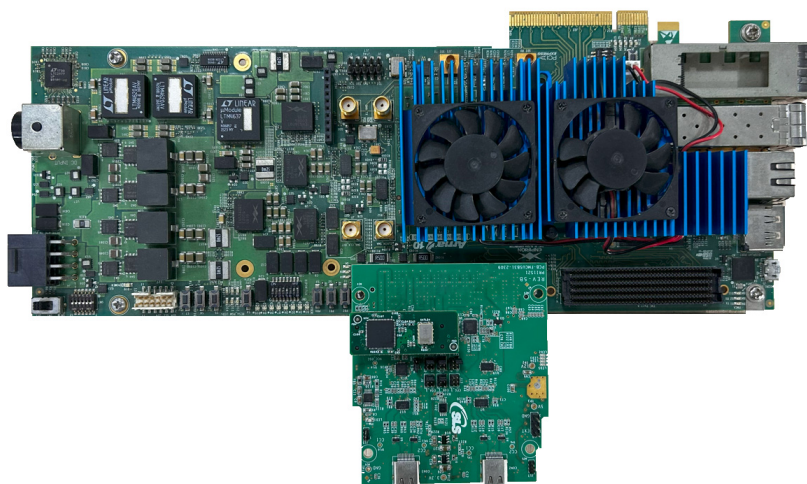
Note: The board does not include any reference design.

Application

- Evaluation of USB IP cores

Application Examples

The following example shows the USB 3.2 FMC connection with FPGA Development Kit.



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