

Download Cable

USB BitJetLite (Full Speed)

The USB BitJetLiteSM (Full Speed) download cable interfaces a USB port on a host computer to an Altera[®] FPGA mounted on a printed circuit board. The cable sends configuration data from the PC to a standard 10-pin header connected to the FPGA. You can use the USB BitJetLite (Full Speed) download cable to iteratively download configuration data to a system during prototyping or to program data into the system during production.



Features

- Supported Devices
 - Stratix[®] series FPGAs
 - Cyclone[®] series FPGAs
 - Arria[®] series FPGAs
 - MAX[®] series CPLDs
- In-system programming for
 - Serial configuration devices like EPCS1, EPCS4, EPCS16, EPCS64 and EPCS128
- Perform Signal Tap[®] II logic analysis

Power Requirements

- The USB BitJet requires the following power sources
 - 5.0 V from the USB cable
 - Between 1.5 V and 3.3 V from the target circuit board

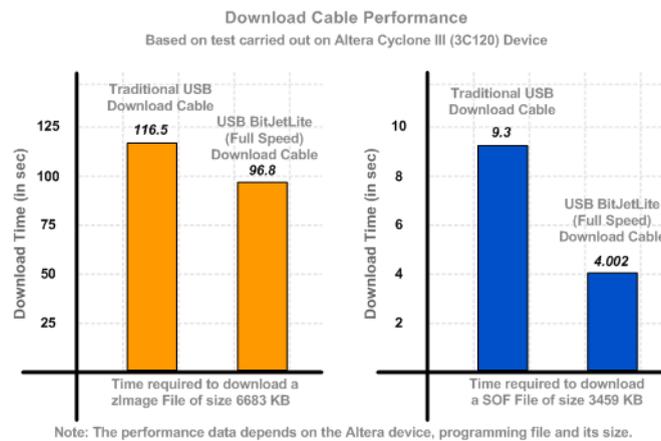
Deliverables

- Installation CD
 - Drivers for Windows XP (32/64 Bit) with Service Pack 2, Windows Vista (32/64 Bit) and Windows 7 (32/64 Bit)
- USB Cable

Supported Tools

- Quartus II Programmer (for programming and configuration)
- Quartus II SignalTap[®] II Logic Analyzer (for logic analysis)
- Quartus II Programmer (standalone version)
- Quartus II SignalTap II logic analyzer (standalone version)

Download Cable Performance



Note: [®] indicates Registered Mark of Altera Products.

For more information <http://www.slscorp.com/products/download-cables/usb-bitjetlite-full-speed.html>

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