



USB 5Gbps Device IP Core (USB3.2SF Gen1 Device Controller) For Arria® V & Cyclone® V FPGA Families

March 2026, Ver. 1.0

Product End of Support (EOS) Notice

Purpose

This notice formally announces the End of Support (EOS) status of the USB 5Gbps Soft IP for Arria® V and Cyclone® V FPGA families, in alignment with standard product life cycle management practices.

- Affected FPGA Families: Arria® V, Cyclone® V
- Vendor Ecosystem: Altera® FPGA

Product Life Cycle Status

The USB 5Gbps Device IP Core for Arria® V and Cyclone® V has reached the End of Life phase of its lifecycle. Declining market demand, coupled with the high engineering effort required to maintain compatibility with ongoing IP core and FPGA tool updates, has made continued support for Arria-V and Cyclone-V economically unsustainable.

Effective Dates

The following milestones outline the key dates and stages defining the transition and support life cycle for the final qualified release.

Milestone	Description	Effective Date
Last Qualified Release	Final supported IP version	usb31sf-v4.2.x.0.0.exe
End of Support (EOS)	No new features or enhancements	01 April 2026
	No further bug fixes or re qualification	30 September 2026

End of Support (EOS) Definition

End of Support indicates the phase where active development, updates, and dedicated engineering assistance for the specified platforms are formally discontinued. After the EOS date-

- Enhancements, bug fixes, compliance updates, and tool-chain re-qualification will be discontinued specific to Arria® V and Cyclone® V.
- Engineering support for new issues specific to Arria® V and Cyclone® V will no longer be available.
- Support specific to Arria® V and Cyclone® V FPGA will be limited to best effort guidance using existing documentation.

Customer Impact and Commitment

SLS is committed to ensure continuity, stability, and long-term support to its customer. However, here is the customer impact for this EOS:

- Customers may continue to deploy and maintain products using eusb31sf-v4.2.x.0.0.exe.
- This final release is considered stable for production and deployed systems.
- No forced migration is required for existing designs.

Recommended Migration Path

To ensure long-term support, enhanced performance, and continued compatibility with evolving development environments, customers are encouraged to consider migrating to currently supported FPGA platforms.

- Agilex™ 7 / Agilex™ 5 / Agilex™ 3 FPGA Families.
- Arria® 10 / Cyclone® 10 / Stratix® 10 FPGA Families.
- Actively supported USB IP solutions aligned with current Altera® toolchain.

Migrating to these supported platforms will help maintain access to ongoing updates, engineering support, and future technology enhancements.

Migration assistance and technical consultation are available upon request.

Legal Notice

The following statements acknowledge the trademark ownership and intellectual property rights associated with the referenced product names and technologies.

- Arria® V, Cyclone® V are registered trademarks of Altera® Corporation.
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Contact

For questions regarding this notice or migration planning, please contact your System Level Solutions (SLS) representative. For any query, write us at info@slscorp.com.

Issued by

This notice is issued by System Level Solutions (SLS) to communicate with their customers and partners regarding product life cycle updates.



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